

CERTIFICATE OF TRA	ANSMISSION BY FAC	SIMILE (37 CFR 1.8)	Docket No. BUR920010086US1
Serial No. 09/683,486	Filing Date 1/7/2002	Examiner Gebremariam, Samuel A.	Group Art Unit 2811
errion: FIN-BASED DO		IRESHOLD CMOS FET WITH	SPACER GATE AND
I hereby certify that this _		Amendment (10 pages) (Identify type of correspondence)	
on 7/11/2003 (Date)		nt and Trademark Office (Fax. N	87218
•		Kim Dwil (Typed or Printed Nume) of Pers	cski ou Signing Certificate)
		bave its own certificate of mailing.	
	Note: Each paper must		FAX RECEIVED
			JUL 1 1 2003
		TE	CHNOLOGY CENTER 2800
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Examiner: Gebremariam, Samuel A.

Art Unit: 2811

For: FIN-BASED DOUBLE POLY DYNAMIC THRESHOLD CMOS FET WITH SPACER GATE AND METHOD OF FABRICATION

Commissioner for Patents
2.0. Box 1450
Nexandria, VA 22313-1450

In response to the Office Action mailed April 23, 2003, please amend the aboveidentified application as follows:

07/17/2003 ASMITH 00000001 090456

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